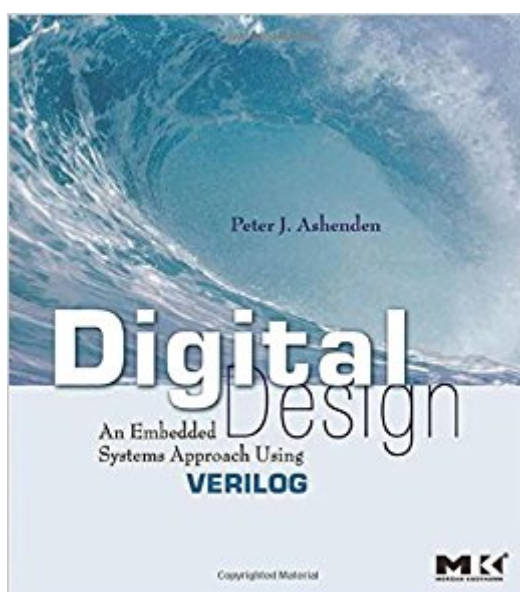**The book was found**

Digital Design (Verilog): An Embedded Systems Approach Using Verilog

Synopsis

Digital Design: An Embedded Systems Approach Using Verilog provides a foundation in digital design for students in computer engineering, electrical engineering and computer science courses. It takes an up-to-date and modern approach of presenting digital logic design as an activity in a larger systems design context. Rather than focus on aspects of digital design that have little relevance in a realistic design context, this book concentrates on modern and evolving knowledge and design skills. Hardware description language (HDL)-based design and verification is emphasized--Verilog examples are used extensively throughout. By treating digital logic as part of embedded systems design, this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components. Includes a Web site with links to vendor tools, labs and tutorials.Presents digital logic design as an activity in a larger systems design contextFeatures extensive use of Verilog examples to demonstrate HDL (hardware description language) usage at the abstract behavioural level and register transfer level, as well as for low-level verification and verification environmentsIncludes worked examples throughout to enhance the reader's understanding and retention of the materialCompanion Web site includes links to tools for FPGA design from Synplicity, Mentor Graphics, and Xilinx, Verilog source code for all the examples in the book, lecture slides, laboratory projects, and solutions to exercises

Book Information

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Customer Reviews

Peter J. Ashenden received his B.Sc.(Hons) and Ph.D. from the University of Adelaide, Australia. He was previously a senior lecturer in computer science and is now a Visiting Research Fellow at the University of Adelaide. His research interests are computer organization and electronic design automation. Dr. Ashenden is also an independent consultant specializing in electronic design automation (EDA). He is actively involved in IEEE working groups developing VHDL standards, is the author of The Designer's Guide to VHDL and The Student's Guide to VHDL and co-editor of the Morgan Kaufmann series, Systems on Silicon. He is a senior member of the IEEE and a member of the ACM.

Pretty decent book for beginners, and has plenty of examples. If you're just learning Verilog and hardware design in general I would suggest it.

This book was kinda hard to follow, and not to interesting since its the 2nd or 3rd digital logic book ive read,

'Twas legit

Ashenden teaches digital design from scratch. The prerequisites are minimal, as perhaps you might be glad to know. While many readers might come from electrical engineering, you don't need much analog circuit experience. Digital circuitry treats the transistors and other components at a very simple but useful level of understanding. In other words, if your background is pure computer science, and no hardware, the book should still work for you.The flow of teaching is fairly standard. Start with Boolean algebra and the simplest circuits that implement these. Then build up from there. [How else can you teach this stuff?] The coverage is thorough, for a first text on the subject. The only slight quirk is that the problems tend to be a little too simple. It's necessary to have easy questions. But some harder problems might also help.The narrative also uses Verilog as the Hardware Description Language. The de facto standard for many, and well worth learning if you're serious about pursing a career in this field.

Excellent,Fast shipping.

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